

FIG. 1  
(Prior Art)

COMPUTER SYSTEM 200

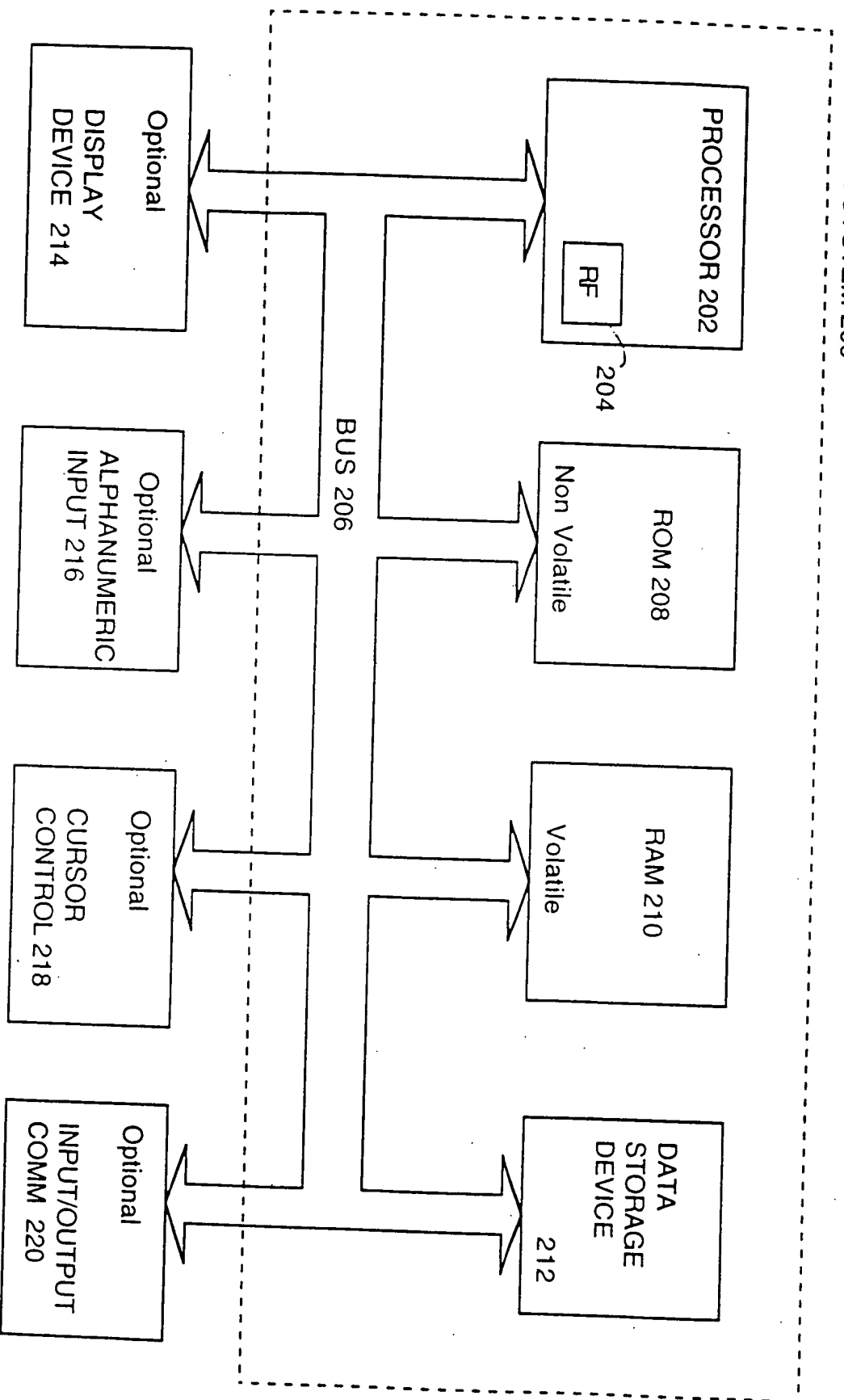


FIG. 2

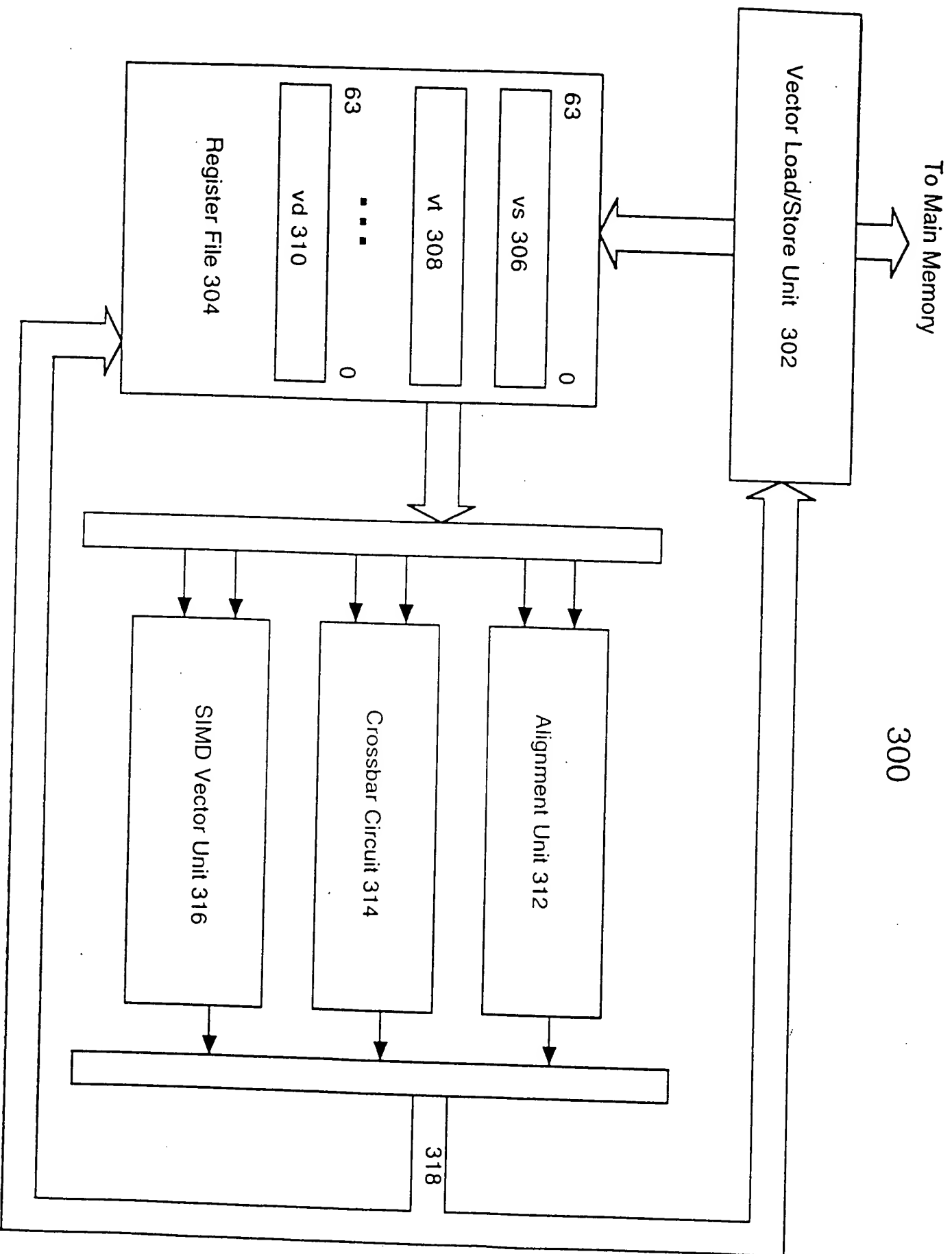
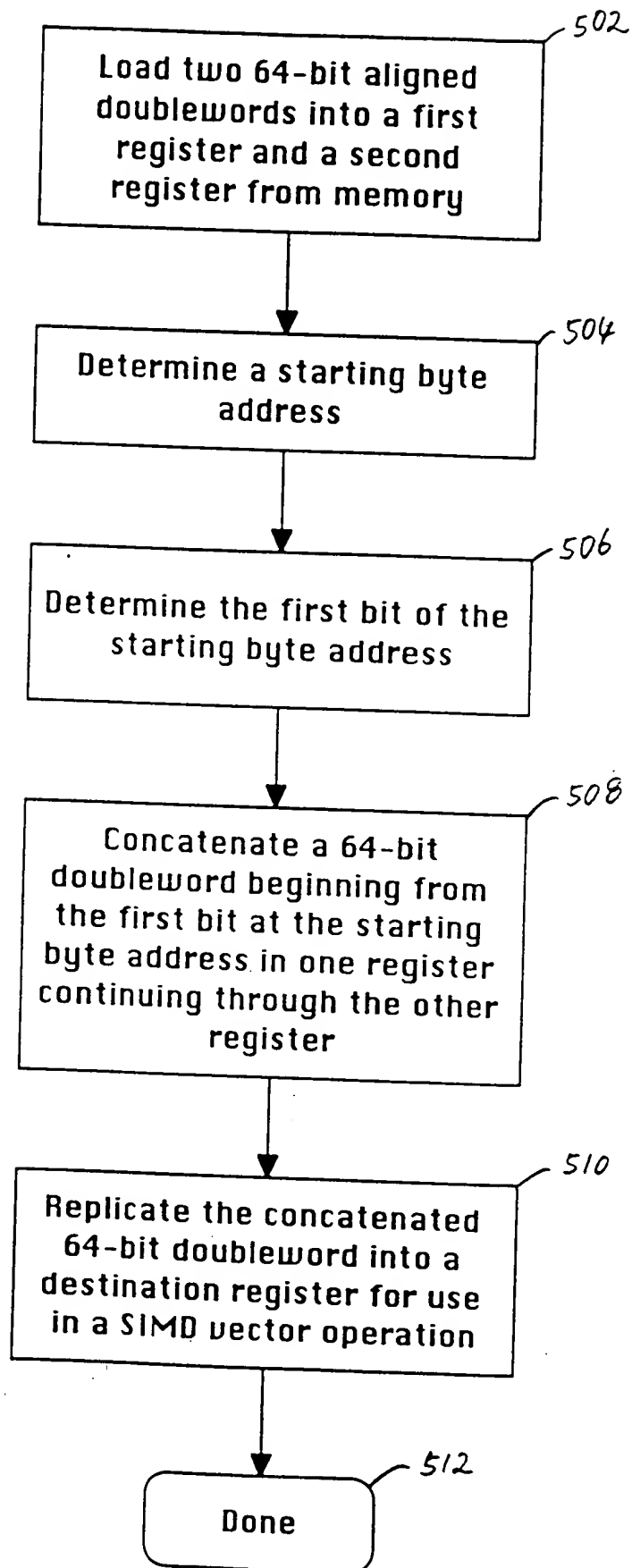


FIG. 3





**FIG. 5**

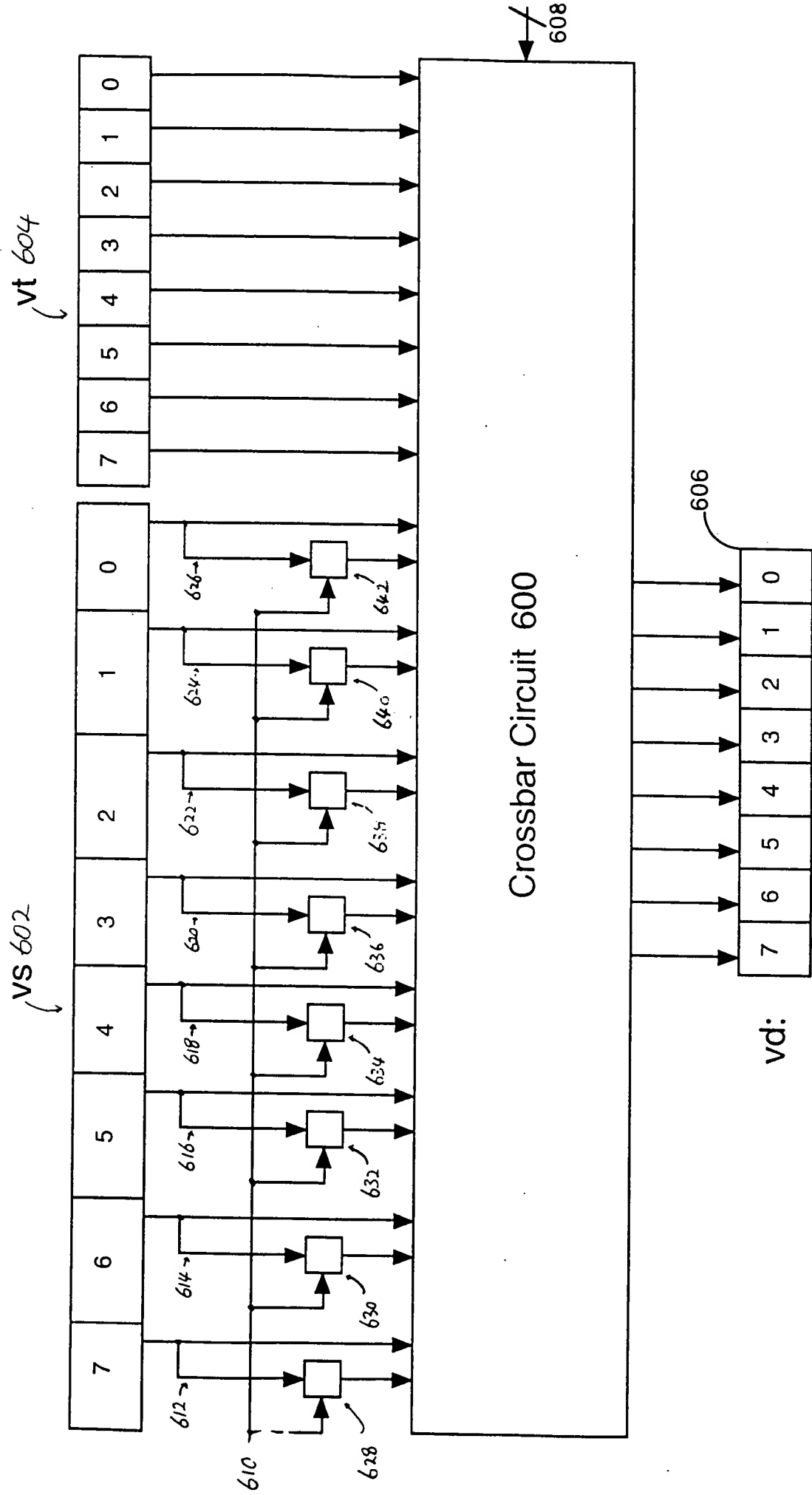


FIG. 6A

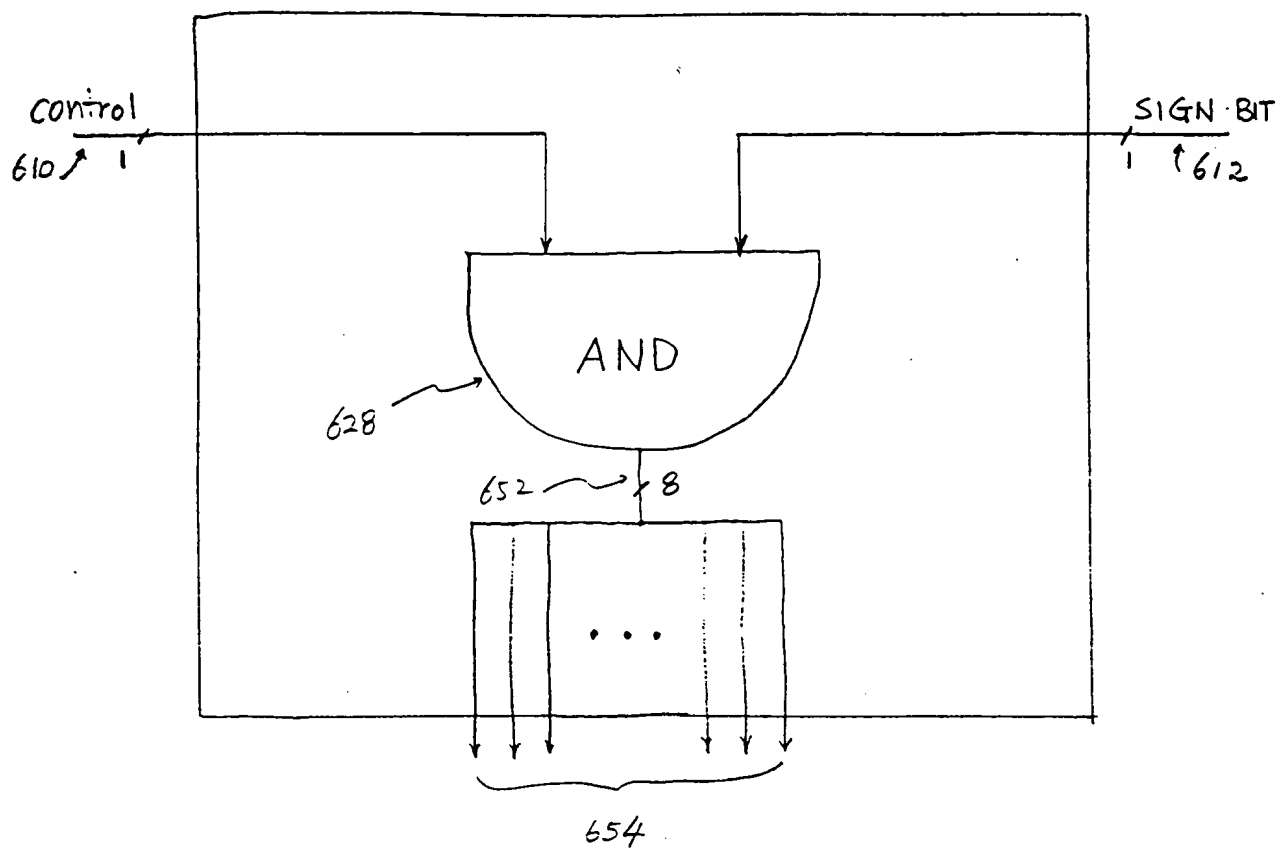


FIG. 6B

63

vd[7]	vd[6]	vd[5]	vd[4]	vd[3]	vd[2]	vd[1]	vd[0]	0	~ 702
0	vs[7]	0	vs[6]	0	vs[5]	0	vs[4]		~ 704
0	vs[3]	0	vs[2]	0	vs[1]	0	vs[0]		~ 706
sign vs[7]	vs[7]	sign vs[6]	vs[6]	sign vs[5]	vs[5]	sign vs[4]	vs[4]		~ 708
sign vs[3]	vs[3]	sign vs[2]	vs[2]	sign vs[1]	vs[1]	sign vs[0]	vs[0]		~ 710
vs[7]	vs[5]	vs[3]	vs[1]	vt[7]	vt[5]	vt[3]	vt[1]		~ 712
vs[6]	vs[4]	vs[2]	vs[0]	vt[6]	vt[4]	vt[2]	vt[0]		~ 714
vs[7]	vt[7]	vs[6]	vt[6]	vs[5]	vt[5]	vs[4]	vt[4]		~ 716
vs[3]	vt[3]	vs[2]	vt[2]	vs[1]	vt[1]	vs[0]	vt[0]		~ 718

FIG. 7



UPUH

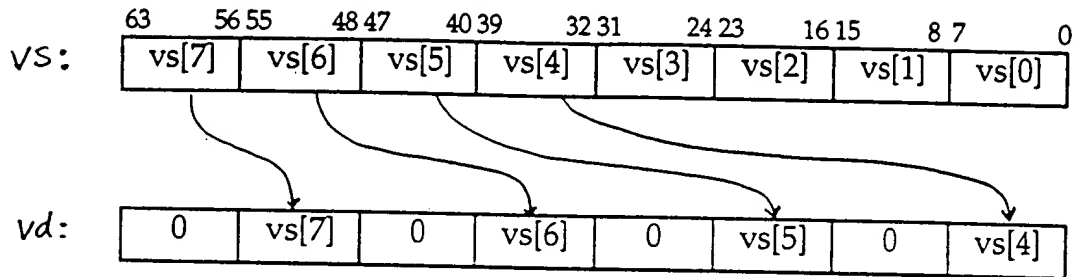


FIG. 8A

UPUL

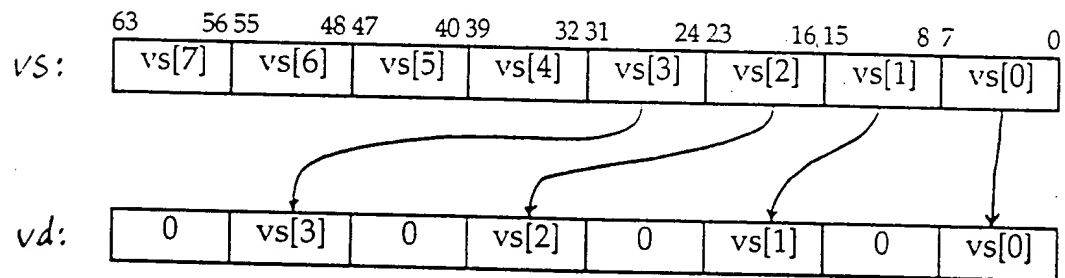


FIG. 8B

# UPSH

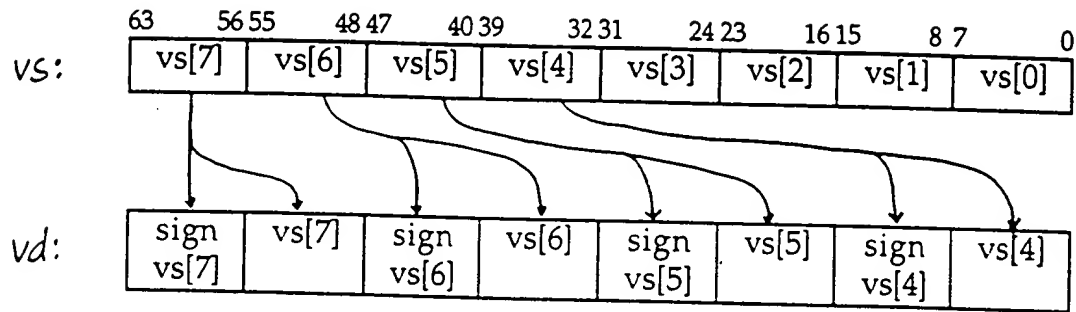


FIG. 8C

# UPSL

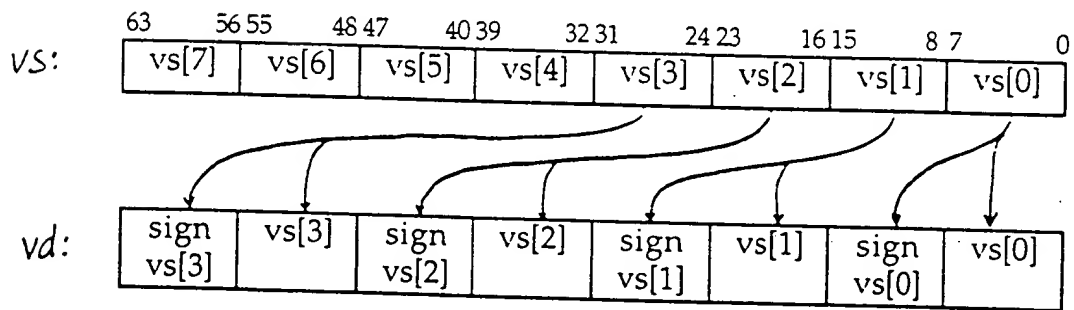


FIG. 8D

PACH

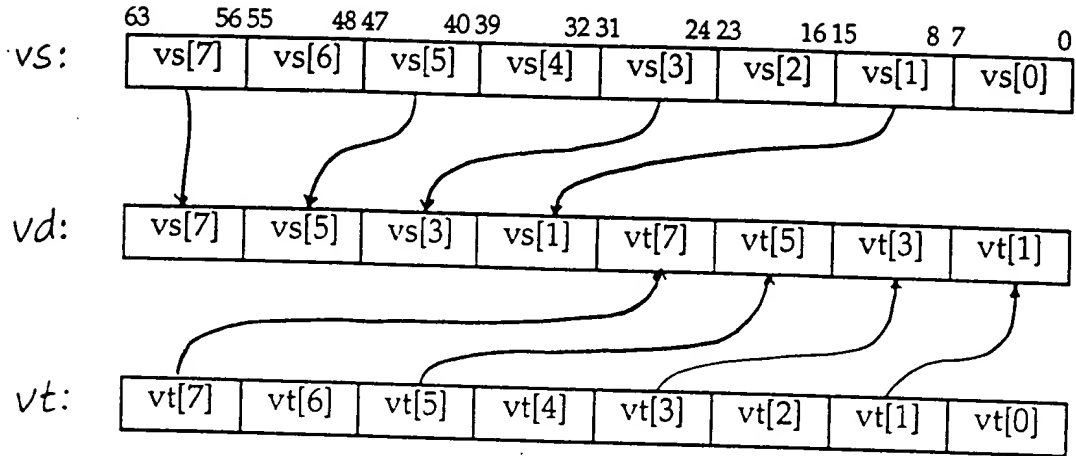


FIG. 8E

PACL

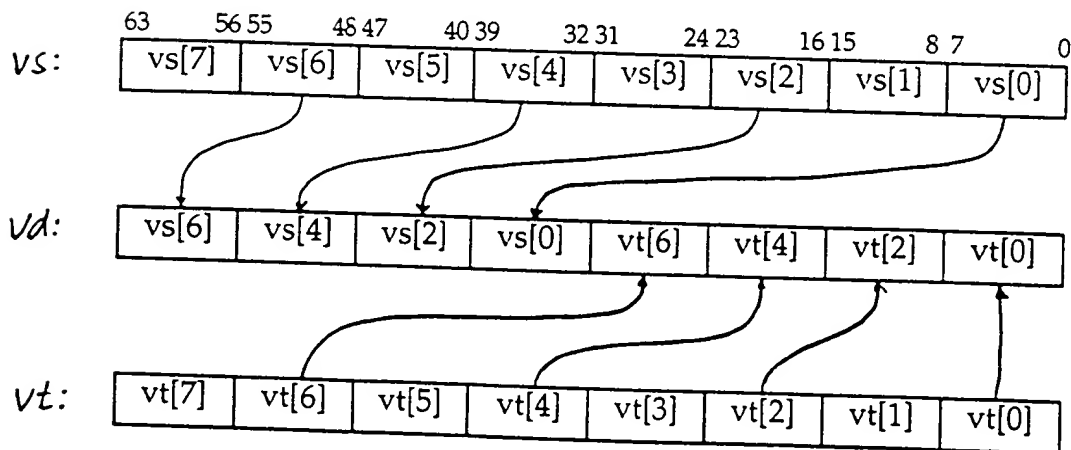


FIG. 8F

# MIXH

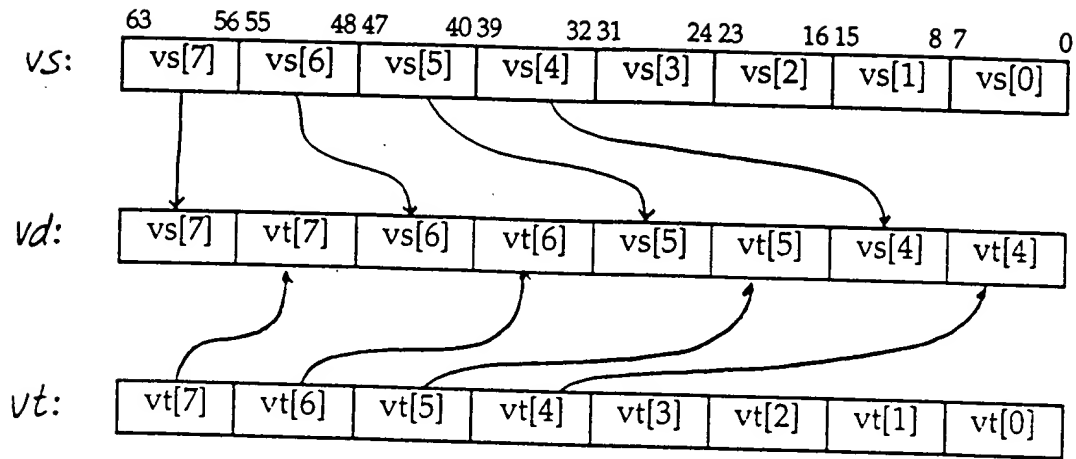


FIG. 8G

# MIXL

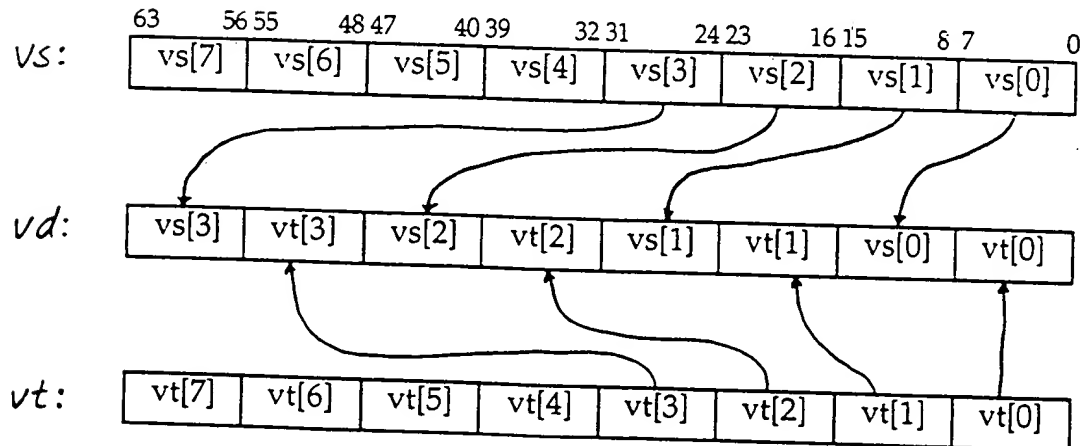


FIG. 8H

63	vd[3]	vd[2]	vd[1]	vd[0]	0	~ 902
	vs[3]	vt[3]	vs[2]	vt[2]		~ 904
	vs[1]	vt[1]	vs[0]	vt[0]		~ 906
	vs[3]	vs[1]	vt[3]	vt[1]		~ 908
	vs[2]	vs[0]	vt[2]	vt[0]		~ 910
	vs[2]	vt[3]	vs[0]	vt[1]		~ 912
	vs[0]	vt[1]	vs[2]	vt[3]		~ 914
	vs[3]	vs[2]	vt[3]	vt[2]		~ 916
	vs[1]	vs[0]	vt[1]	vt[0]		~ 918

FIG. 9

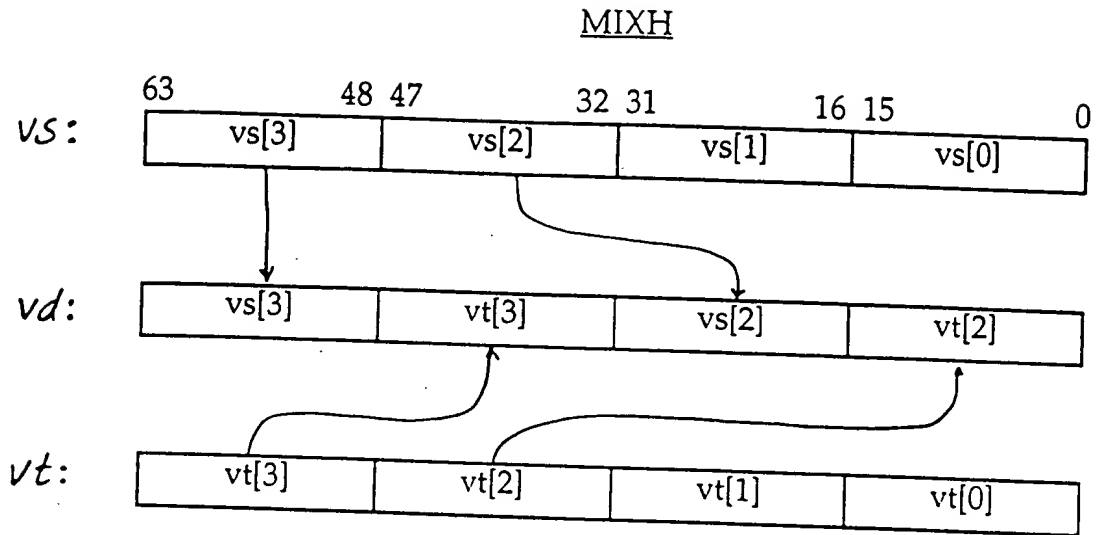


FIG. 10A

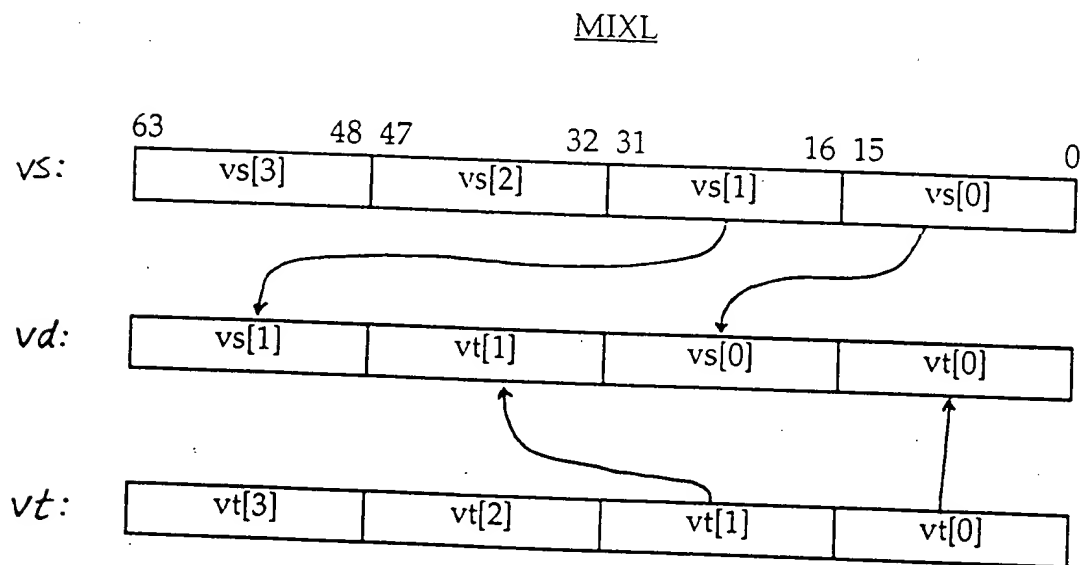


FIG. 10B

PACH

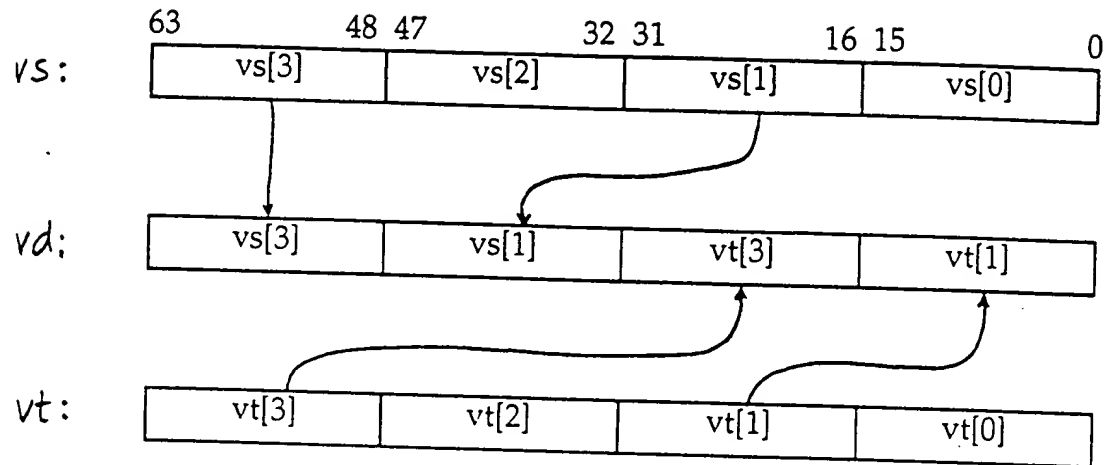


FIG. 10C

PACL

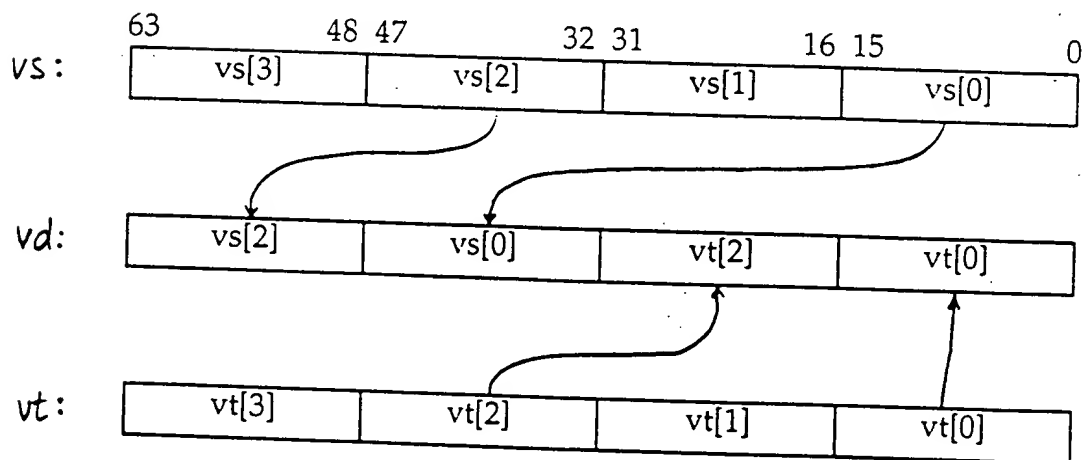


FIG. 10D

BFLA

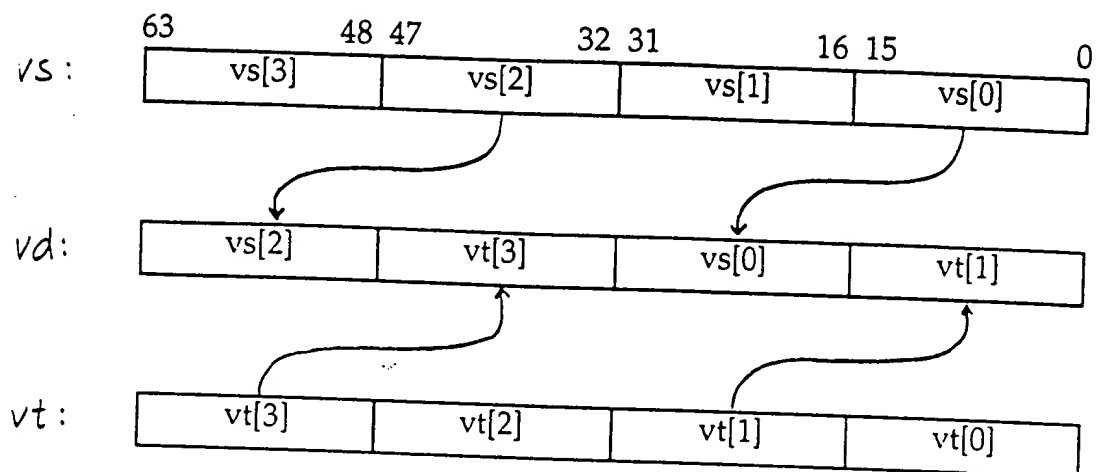


FIG. 10E

BFLB

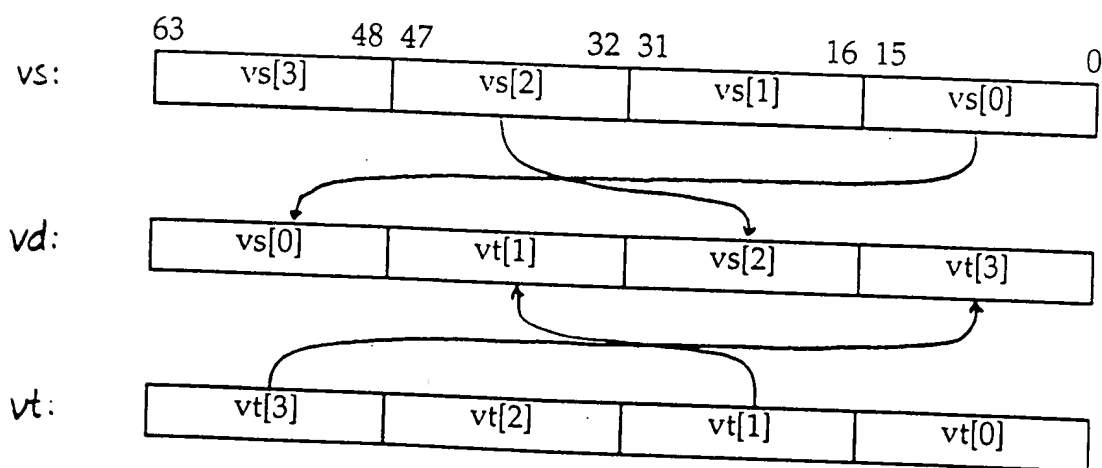


FIG. 10F



REPA

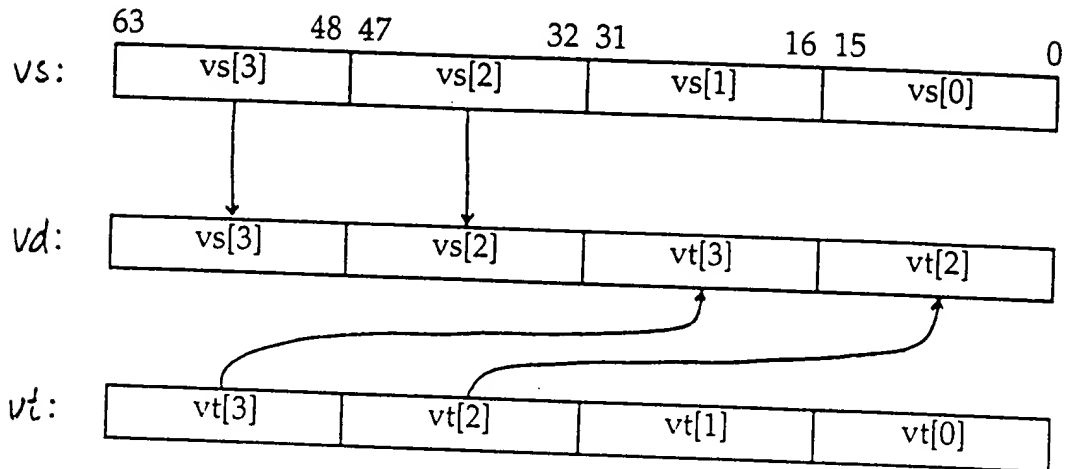


FIG. 10G

REPB

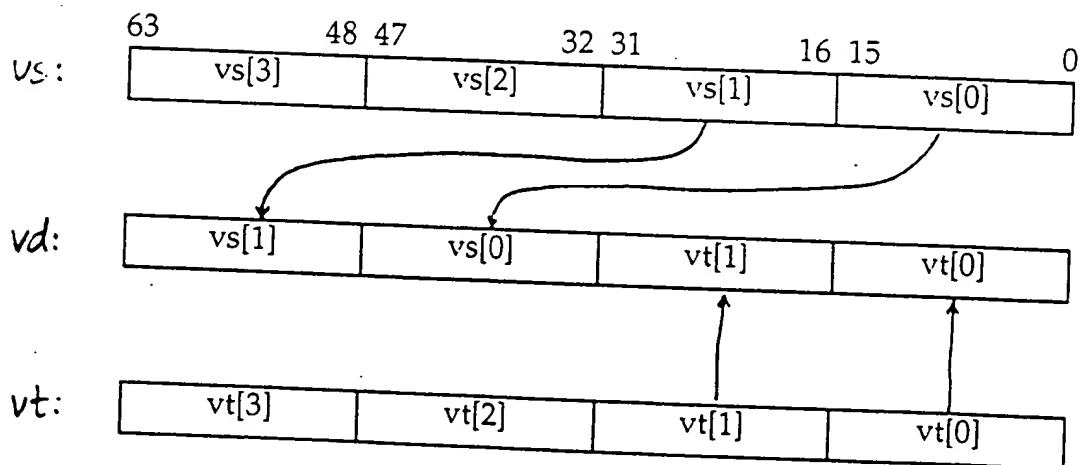


FIG. 10H